

FIG. 1

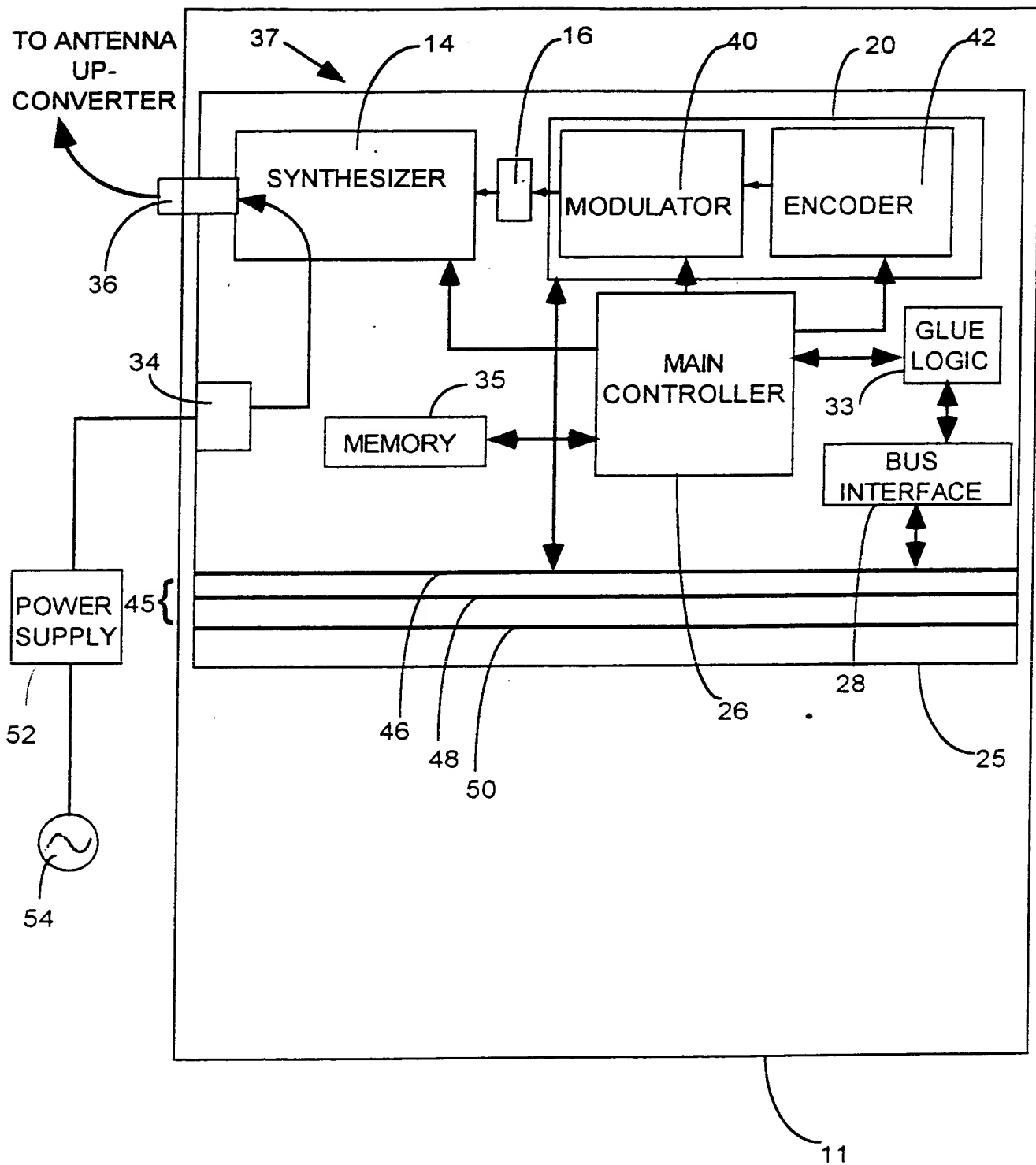


FIG. 2

FIG. 3 is a block diagram of a system 11, which includes a first module 37 and a second module 56. The first module 37 includes a synthesizer 14, a modulator 16, a receiver interface 16, an encoder 20, a main controller 31, a memory 35, a glue logic 33, and a bus interface 28. The second module 56 includes a tuner 51, an ASIC 53, an MPEG2 demux & decoder 59, a RAM 57, and a bus I/F 55. The system 11 is connected to a power supply 52 and an antenna up-converter. The system 11 is also connected to a low noise block 54. The system 11 is connected to a bus 45, which includes a data bus 46, an address bus 48, and a control bus 50. The system 11 is connected to a bus 54, which includes a data bus 56, an address bus 58, and a control bus 60. The system 11 is connected to a bus 59, which includes a data bus 61, an address bus 63, and a control bus 65.

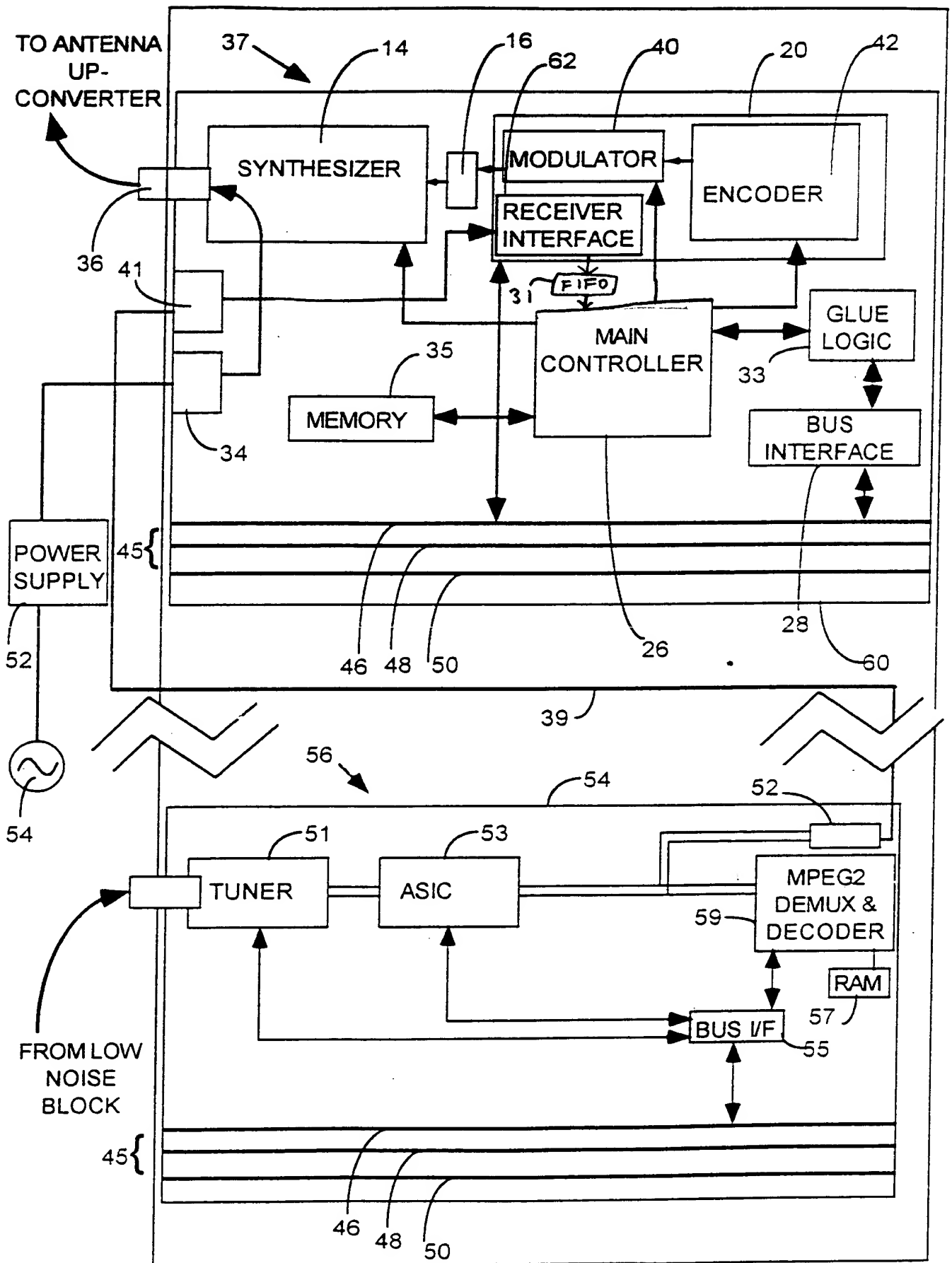


FIG. 3